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500.45008X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: NEGISHI et al

Serial No.: 10/531,700

Filed: April 18, 2005

For: Method For Fabricating Semiconductor Device

Art Unit:

Examiner:

PRELIMINARY AMENDMENT

Mail Stop: Amendment (No Fee)
Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

November 9, 2005

Sir:

The following amendments are respectfully requested in connection with the above-identified application, as listed below and as set forth on the following pages:

Amendments to the Claims; and

Remarks are included following the amendments.